



PATENT
1248-0681P

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: Yoshihisa FUJIMOTO Conf.: UNKNOWN
Appl. No.: 10/724,901 Group: UNKNOWN
Filed: December 2, 2003 Examiner: UNKNOWN
For: SWITCHED CAPACITOR CIRCUIT

L E T T E R

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

January 12, 2004

Sir:

Applicant(s) filed an Information Disclosure Statement (IDS) on December 2, 2003, in which the filing dates for U. S. Patent Nos. 4,920,325 and 6,437,608 were incorrectly listed as April 1, 1990 and August 1, 2002, respectively, on the Form PTO-1449(s). A corrected Form PTO-1449 is attached hereto showing the correct filing dates for U. S. Patent Nos. 4,920,325 and 6,437,608 as April 24, 1990 and August 20, 2002, respectively. Please substitute this corrected Form PTO-1449 for the incorrect one previously submitted with the IDS filed on December 2, 2003 and return an appropriately initialed copy to Applicant(s).

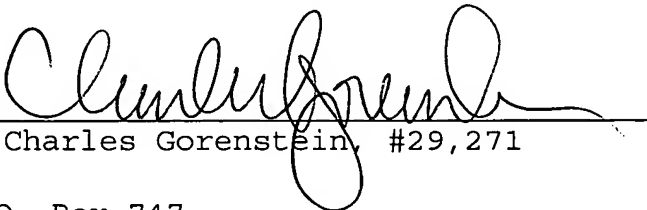
Appl. No. 10/724,901

If the Examiner has any questions concerning this application, the Examiner is requested to contact the undersigned at (703) 205-8000 in the Washington, DC area.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. § 1.16 or under 37 C.F.R. § 1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

By 
Charles Gorenstein, #29,271

CG/DKD/slb
1248-0681P

Attachment(s)

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Form PTO-144

**INFORMATION DISCLOSURE CITATION
IN AN APPLICATION**

(Use several sheets if necessary)

ATTY. DOCKET NO.
1248-0381PAPPLICATION NO.
10/724,901APPLICANT
Yoshihisa FUJIMOTOFILING DATE
December 2, 2003GROUP
UNKNOWN**U.S. PATENT DOCUMENTS**

EXAMINER INITIAL	DOCUMENT NUMBER	Kind	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	US 4,920,325		1990-04-24	NICOLLINI, et al.			
	US 6,437,608		2002-08-20	MIYABE, et al.			
	US						
	US						
	US						
	US						
	US						
	US						
	US						
	US						

FOREIGN PATENT DOCUMENTS

	Office	DOCUMENT NUMBER	Kind	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION	
								YES	NO

OTHER DOCUMENTS

(Include Name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.) date, page(s), volume-issue number(s), publisher, city and/or country where published.)

"ANALOG MOS INTEGRATED CIRCUITS FOR SIGNAL PROCESSING", R. Gregorian, et al.,
published 1986, pages 513-524.

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.